Post-Doctoral Research Fellow  
(VLSI Circuits and Systems)  
University of South Florida

Position Description:

The Laboratory of Green and secure Integrated Circuits and Systems (LOGICS) at the University of South Florida is searching for a Post-Doctoral Research Fellow with expertise in VLSI Circuits and Systems. Knowledge of test-chip design, emerging technologies and/or security (cryptography and hardware security) is a plus. The Fellow will work closely with Prof. Swaroop Ghosh and actively advise PhD students in LOGICS lab.

The position will be available immediately and for a period of up to three years. Newly graduated or graduating PhD students from the areas of VLSI and hardware security are especially encouraged to apply.

Job Requirements:

- PhD in Electrical and Computer Engineering, Computer Science, or related field
- Excellent technical English writing and oral communication skills
- Extensive knowledge and research in the areas of VLSI Circuits and systems

Application Procedure:

- Interested applicants should send CVs and supporting information (relevant publications, etc.) to Prof. Swaroop Ghosh (swaroopghosh@mail.usf.edu)
- Only short-listed candidates will be notified for interview
- Application closes when the position is filled

About PI Ghosh and LOGICS Lab (http://www.cse.usf.edu/~sghosh):

At LOGICS, we are exploring CMOS and emerging post-CMOS nano-technologies for reliable, secure and energy-efficient computations. The lab is led by PI Swaroop Ghosh who received his B.E. (Hons.) from IIT, Roorkee (2000), M.S. from Univ of Cincinnati (2004) and Ph.D. from Purdue University (2008). He joined USF in Fall 2012. Dr. Ghosh was senior research and development engineer in Advanced Design, Intel Corp from 2008 to 2012. His research interests include low-power circuit design, hardware security and digital testing. He is a senior member of IEEE.

Dr. Ghosh is serving as Associate Editor of IEEE TRANSACTIONS ON CIRCUITS AND SYSTEMS-I and guest editor of IEEE JOURNAL ON EMERGING AND SELECTED TOPICS IN CIRCUITS AND SYSTEMS. He has served in the technical program committees of DAC, DATE, ICCAD, ISLPED, Nanoarch, VLSI Design, ISQED, ASQED, and VLSI-SOC. He is a recipient of USF Outstanding Researcher Award (2015).