**Graduate/Undergraduate Research Assistant Wanted**

If you are a graduate or junior/senior undergraduate student who is willing to learn new knowledge and skills, you are invited to join Dr Zheng to participate in research in systems-on-chip design validation. Depending on your background, you will be developing a research systems-on-chip prototype at different scales. The required and desired background/skills are listed below.

**Required:** the following CSE courses or equivalent
- CDA 3201 Logic Design
- CDA 3103 Computer Organization
- CDA 4205 Computer Architecture or equivalent

**Desired:** knowledge of VHDL simulation and synthesis, (Xilinx) FPGA design flow.

Other than hardware related projects, another research area of Dr Zheng is in using formal methods to verify complex concurrent/parallel hardware/software systems in safety critical applications such as aviation, medical devices, etc. If you have strong background in algorithms, programming, software development, and are comfortable with formal logic, you are cordially invited to join his research.

If you are interested, please email Dr Zheng at haozheng@usf.edu for a meeting.